CRTK060P02S2P

Trench P-MOSFET -20V, 3.9mΩ, -80A

Features

- Uses CRM advanced Trench technology
- Extremely low on-resistance $R_{DS(on)}$
- Excellent Q_gx$R_{DS(on)}$ product(FOM)
- Qualified according to JEDEC criteria

Applications

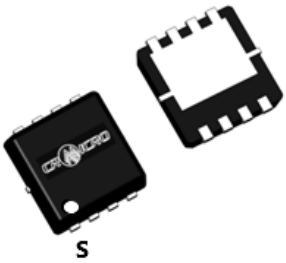
- Motor control and drive
- Electrical tools
- Lithium battery protection

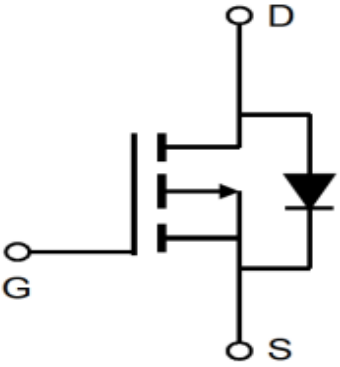
Product Summary

V_{DS}	-20V
$R_{DS(on)}$ typ.	3.9mΩ
I_D	-80A

100% DVDS Tested

100% Avalanche Tested





Package Marking and Ordering Information

Part #	Marking	Package	Packing	Reel Size	Tape Width	Qty
CRTK060P02S2P	060P02S	PDFN3.3×3.3	Reel	N/A	N/A	5000pcs

Absolute Maximum Ratings

Parameter	**Symbol**	**Value**	**Unit**
Drain-source voltage	V_{DS}	-20	V
Continuous drain current T_C = 25°C （Silicon limited） T_C = 25°C （Package limited） T_C = 100°C （Package limited）	I_D	 -80 -50 -50.0	A
Pulsed drain current (T_C = 25°C, t_p limited by T_{jmax})	$I_{D\ pulse}$	-200	A
Avalanche energy, single pulse (L=0.5mH)	E_{AS}	100	mJ
Gate-Source voltage	V_{GS}	±10	V
Power dissipation (T_C = 25°C)	P_{tot}	41.6	W
Operating junction and storage temperature	T_j , T_{stg}	-55...+150	°C
Soldering temperature, wave soldering only allowed at leads (1.6mm from case for 10s)	T_{sold}	260	°C

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Thermal Resistance

Parameter	Symbol	Typ	Max	Unit
Thermal resistance, junction – case.	R_{thJC}	-	3.00	°C/W
Thermal resistance, junction – ambient(min. footprint)	R_{thJA}[a2]	-	62.5	

Electrical Characteristic (at Tj = 25 °C, unless otherwise specified)

Parameter	Symbol	Value			Unit	Test Condition
		min.	typ.	max.		
Static Characteristic						
Drain-source breakdown voltage	BV_{DSS}	-20	-	-	V	V_{GS}=0V, I_D=-250uA
Gate threshold voltage	$V_{GS(th)}$	-0.4	-0.7	-1	V	V_{DS}=V_{GS}, I_D=-250uA
Zero gate voltage drain current	I_{DSS}	-	-	-1	μA	V_{DS}=-20V, V_{GS}=0V
		-	-	-100		T_j=25°C / T_j=150°C
Gate-source leakage current	I_{GSS}	-	-	±100	nA	V_{GS}=±10V,V_{DS}=0V
Drain-source on-state resistance	$R_{DS(on)}$	-	3.9	5.0	mΩ	T_j=25°C, V_{GS}=-4.5V, I_D=-19A
		-	5.5	7.2		V_{GS}=-2.5V, I_D=-19A
Transconductance	g_{fs}	-	80.22	-	S	V_{DS}=-5V, I_D=-19A
Dynamic Characteristic						
Input Capacitance	C_{iss}	-	6234	-	pF	VGS=0V, VDS=-10V f=1MHz
Output Capacitance	C_{oss}	-	1017	-		
Reverse Transfer Capacitance	C_{rss}	-	876	-		
Gate Total Charge	Q_g	-	62.6	-	nC	VGS=-4.5V, VDS=-10V ID=-19A, f=1MHz
Gate-Source charge	Q_{gs}	-	7	-		
Gate-Drain charge	Q_{gd}	-	19	-		
Turn-on delay time	$t_{d(on)}$	-	20	-	ns	VGS=-4.5V, VDS=-10V RG=3.0Ω, ID=-19A
Rise time	t_r	-	50	-		
Turn-off delay time	$t_{d(off)}$	-	272	-		
Fall time	t_f	-	236	-		
Gate resistance	R_G	-	5.3	-	Ω	V_{GS}=0V, V_{DS}=0V f=1MHz

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Body Diode Characteristic

Parameter	Symbol	Value			Unit	Test Condition
		min.	typ.	max.		
Body Diode Forward Voltage	V_{SD}	-	-	-1.2	V	V_{GS}=0V, I_{SD}=-19A
Body Diode Continuous Forward Current	I_S	-	-	-80	A	T_C = 25°C
Body Diode Reverse Recovery Time	t_{rr}	-	243	-	ns	I_F=-19A, dI/dt=-100A/μs
Body Diode Reverse Recovery Charge	Q_{rr}	-	591	-	nC	

a1：Calculated continuous current based on maximum allowable junction temperature. Note that current limitations arising from heating of the device leads may occur with some lead mounting arrangements.
a2：The value of R_{thJA} is measured by placing the device in a still air box which is one cubic foot.

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Typical Performance Characteristics

Fig 1: Output Characteristics

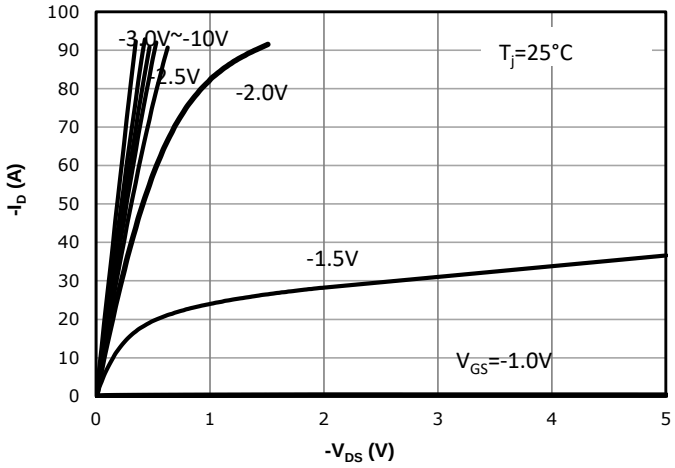


Fig 2: Transfer Characteristics

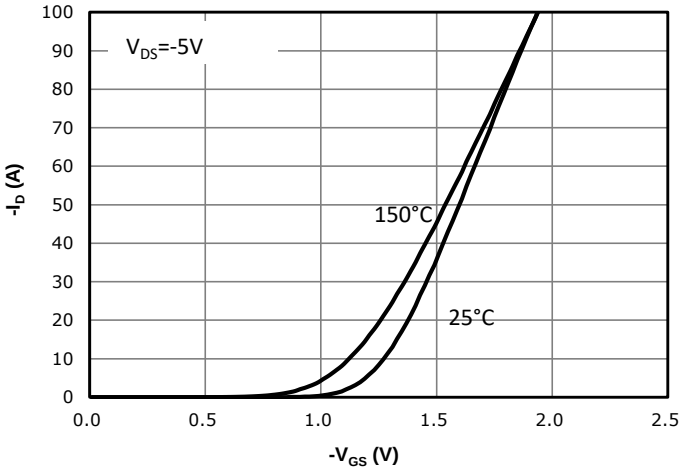


Fig 3: Rds(on) vs Drain Current and Gate Voltage

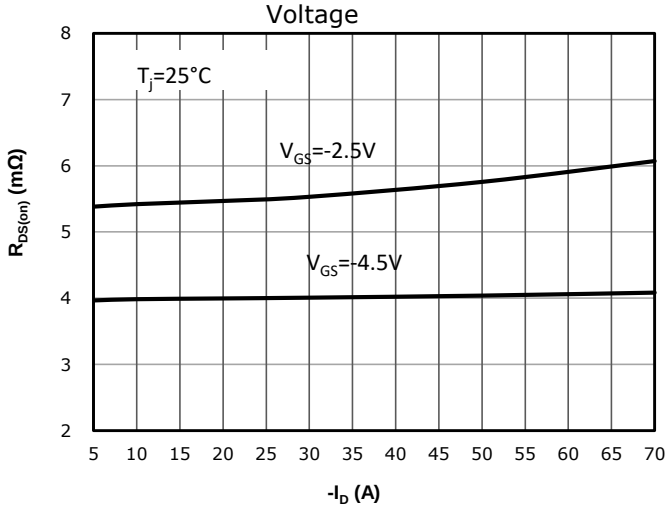


Fig 4: Rds(on) vs Gate Voltage

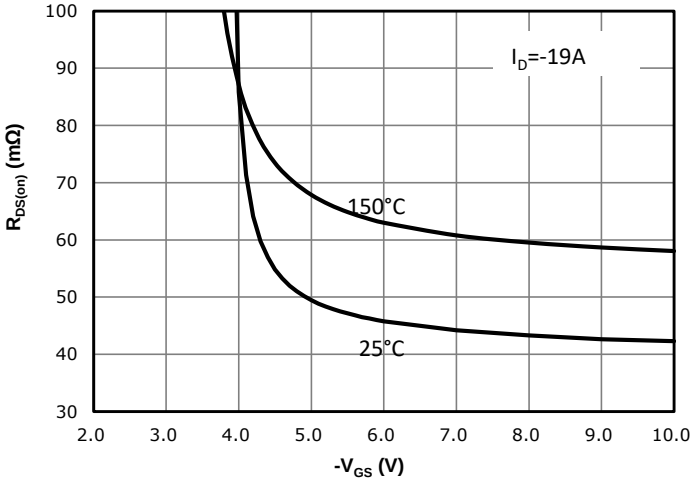


Fig 5: Rds(on) vs. Temperature

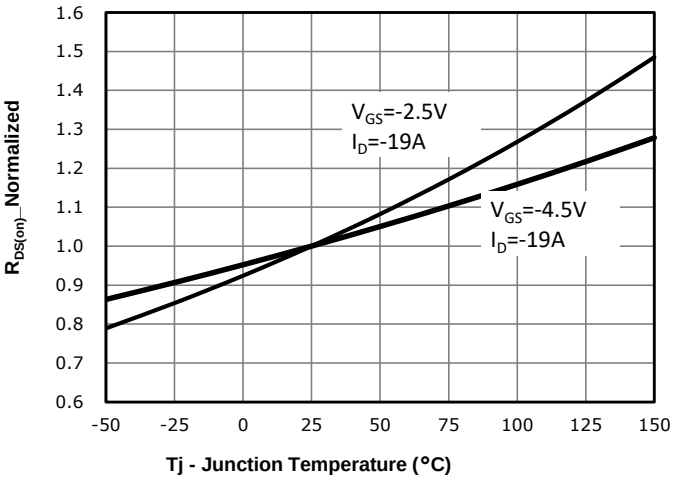
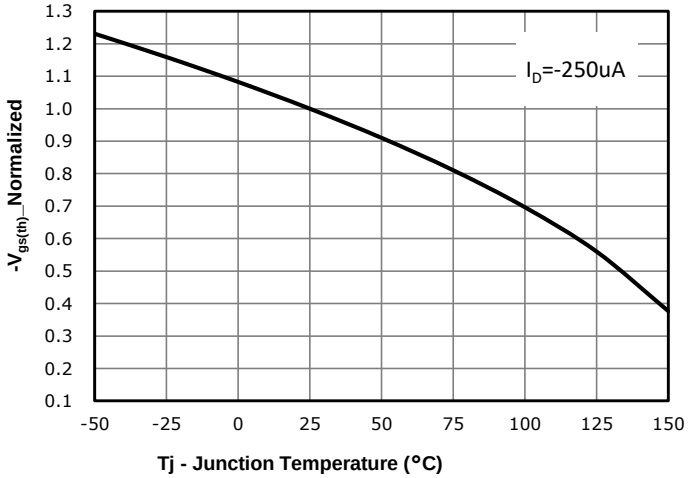


Fig 6: Vgs(th) vs. Temperature



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Fig 7: BVdss vs. Temperature

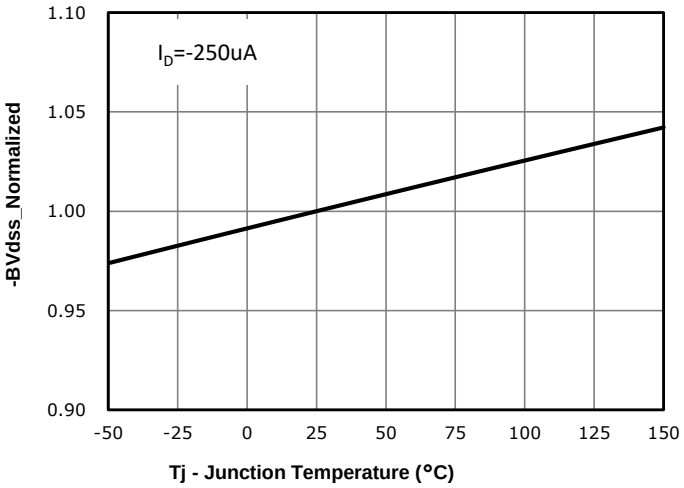


Fig 8: Body-diode Forward Characteristics

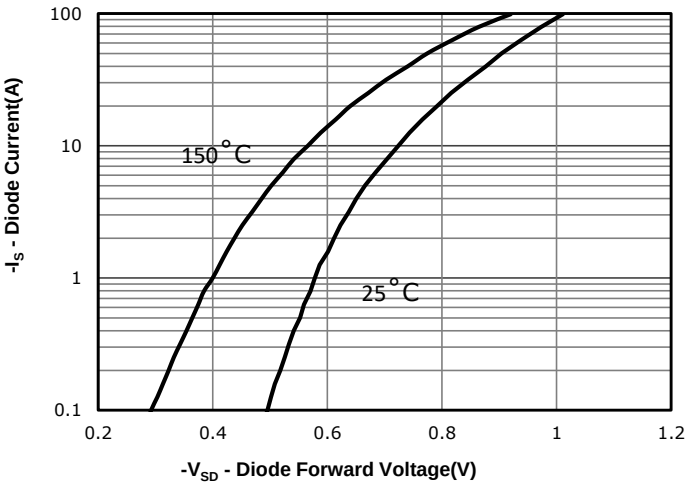


Fig 9: Gate Charge Characteristics

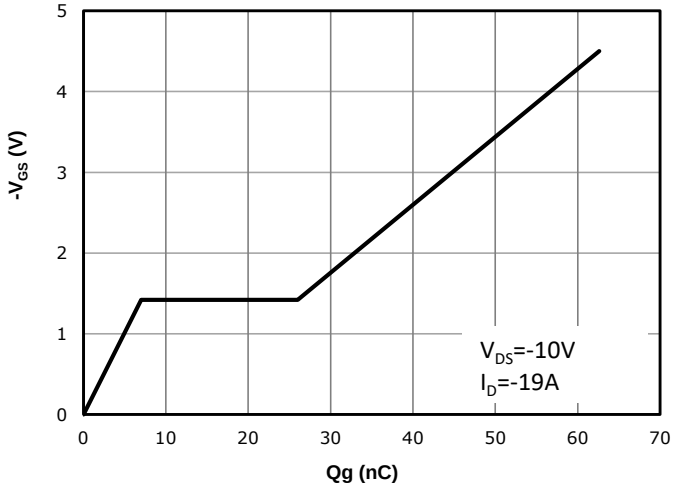


Fig 10: Capacitance Characteristics

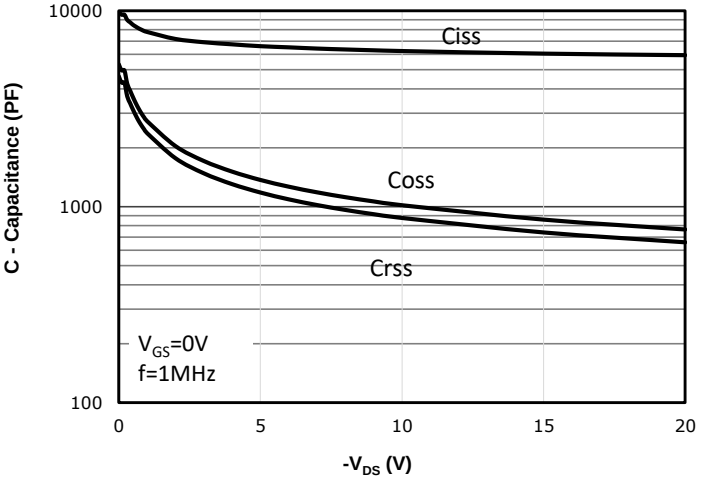


Fig 11: Drain Current Derating

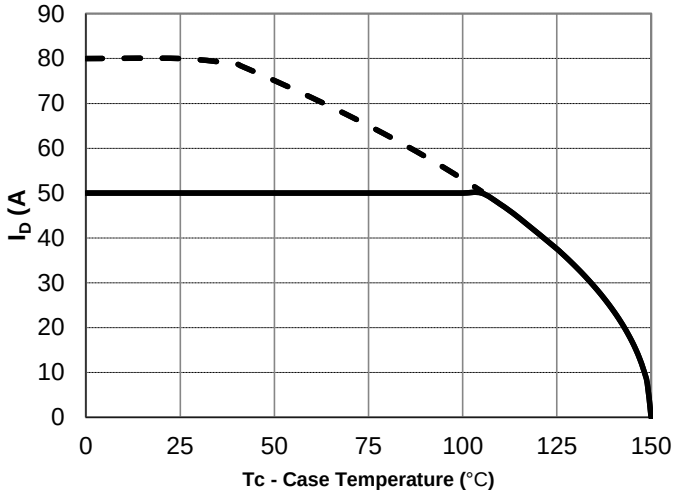
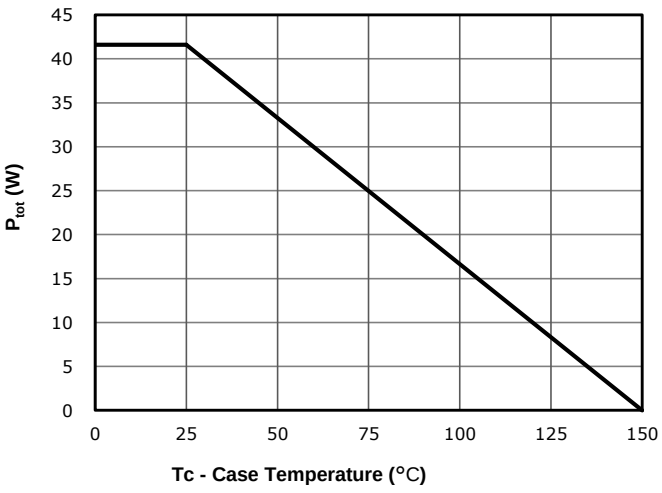


Fig 12: Power Dissipation



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Fig 13: Safe Operating Area

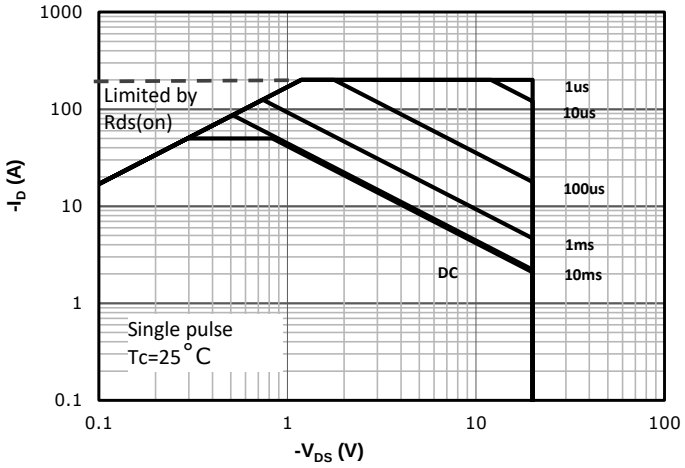
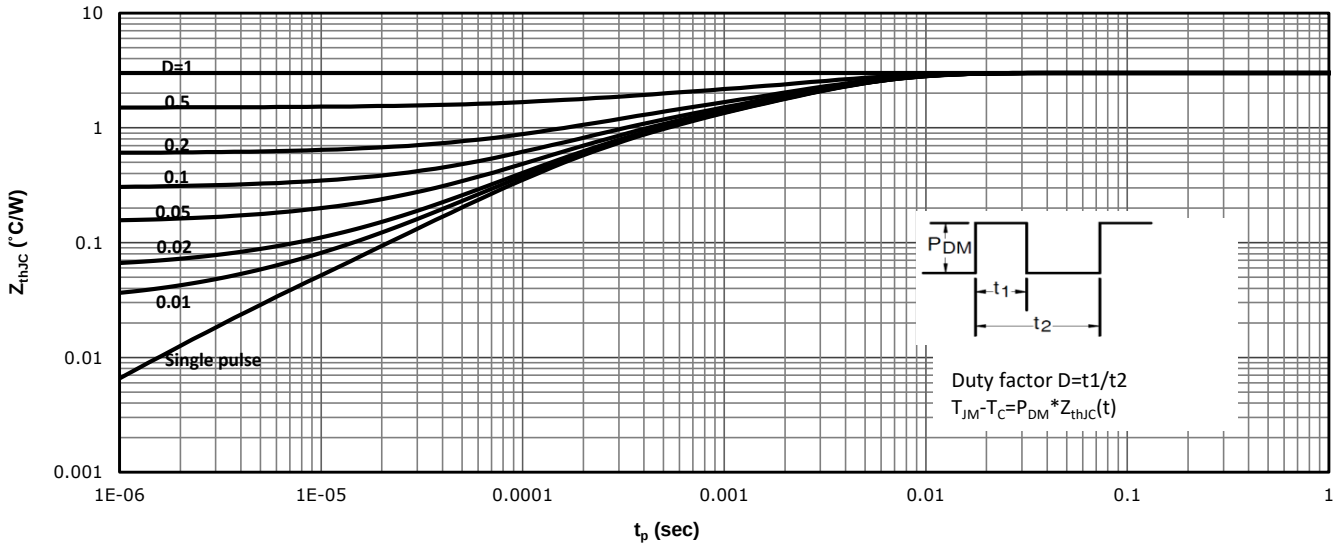


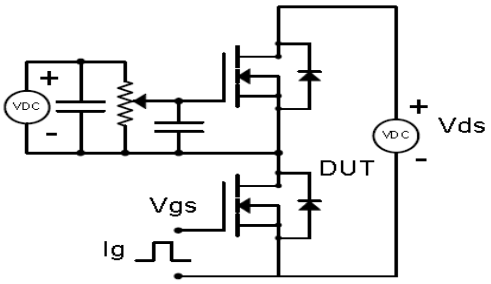
Fig 14: Max. Transient Thermal Impedance

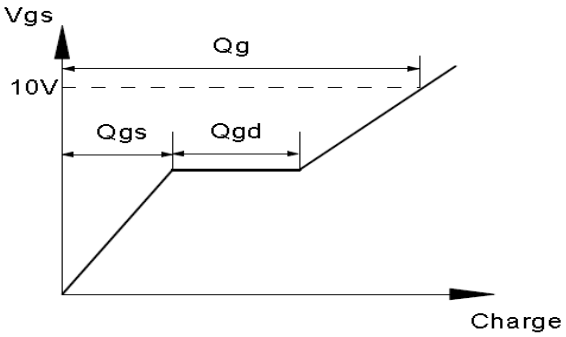


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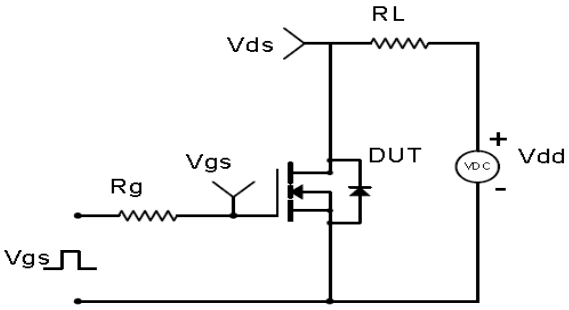
Test Circuit & Waveform

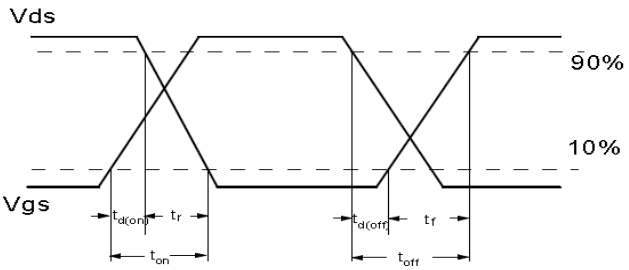
Gate Charge Test Circuit & Waveform



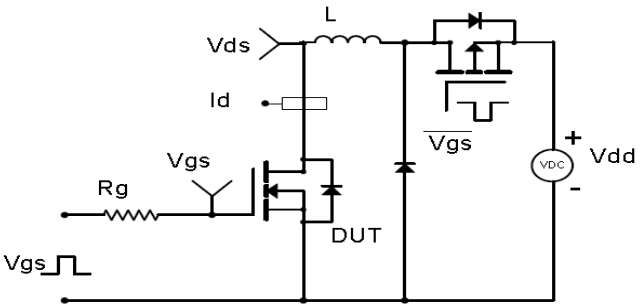


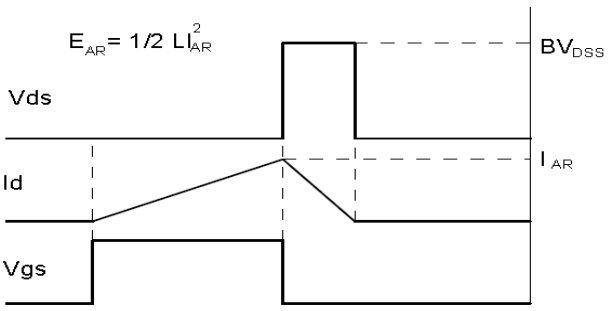
Resistive Switching Test Circuit & Waveforms



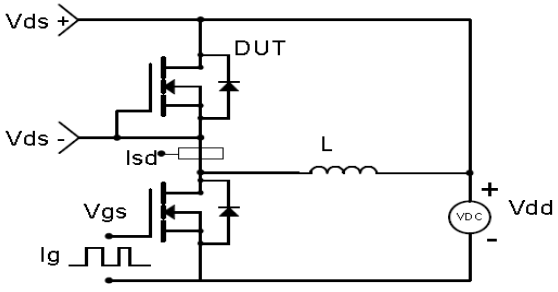


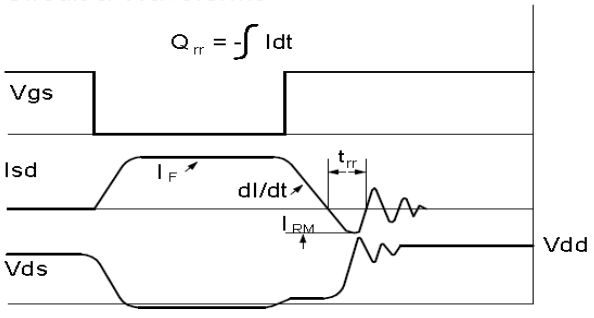
Unclamped Inductive Switching (UIS) Test Circuit & Waveforms





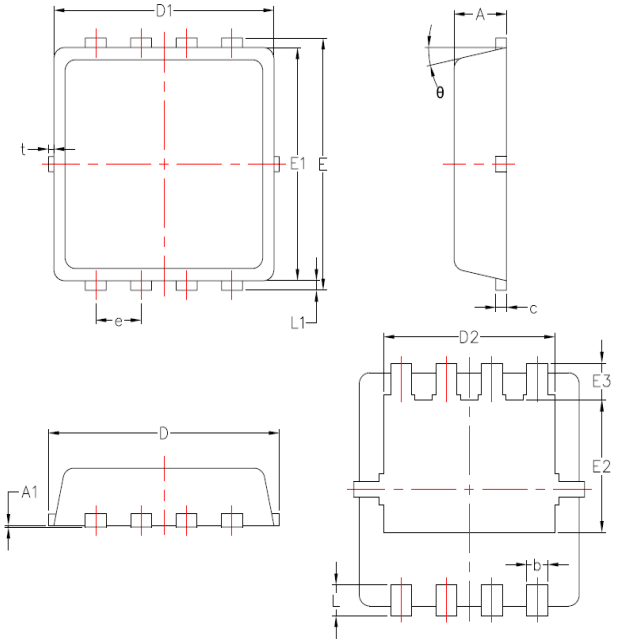
Diode Recovery Test Circuit & Waveforms





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Package Outline: PDFN3.3×3.3



	Values(mm)	
	min	**max**
A	0.70	0.90
A1	/	0.05
b	0.20	0.40
c	0.10	0.25
D	3.05	3.55
D1	2.90	3.35
D2	2.15	2.65
E	3.05	3.55
E1	2.80	3.30
E2	1.54	1.94
E3	0.18	0.65
e	0.55	0.75
L	0.20	0.60
L1	0.05	0.25
t	/	0.15
θ	8°	14°

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Marking



NOTE:
AABXXX
AA —cycle code
B —Fab code
XXX —Assembly lot code

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Revision History

Revison	Date	Major changes
1.0	2022/6/1	Relaease of formal version
2.0		
3.0		

Disclaimer

Unless otherwise specified in the datasheet, the product is designed and qulified as a standard commercial product and is not intended for use in applications that require extraordinary levels of quality and reliability, such as automotive, aviation/aerospace and life-support devices or systems.

Any and all semicondutor products have certain probability to fail or malfunction, which may result in personal injury, death or property damage. Customer are solely responsible for providing adequate safe measures when design their systems.

CRM reserves the right to improve product design, function and reliability without notice.

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