

General Description

This product family is CRM's third generation SiC JBS, with lower V_F and offers state of the art performance. It is designed for high frequency applications where high efficiency and high reliability are required. It is qualified and manufactured on the productive 6 inch SiC line in China fully owned by CR MICRO.

Product Summary

V_{RRM}	650 V
$I_F (T_C=167^{\circ}C)$	10 A
Q_C	25 nC

Features

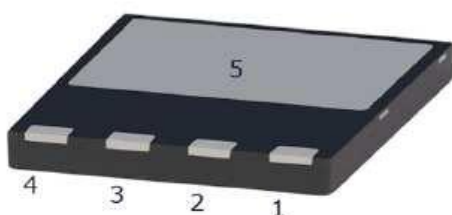
- Low conduction loss due to low V_F
- Extremely low switching loss by tiny Q_C
- Highly rugged due to better surge current
- Industrial standard quality and reliability



Applications

- Server
- Telecom
- High performance SMPS
- Power factor correction

DFN8*8



Equivalent circuit



1,2: NC
3,4: Anode
5: Cathode

Package Marking and Ordering Information

Part #	Marking	Package
CRXN10D065G3	X10D065G3	DFN8*8

Maximum Ratings (at $T_C = 25\text{ }^{\circ}\text{C}$, unless otherwise specified)

Parameter	Symbol	Value	Unit
Repetitive Peak Reverse Voltage	V_{RRM}	650	V
Surge Peak Reverse Voltage	V_{RSM}	650	V
DC Peak Reverse Voltage	V_R	650	V
Continuous Forward Current $T_C = 25^{\circ}\text{C}$ $T_C = 135^{\circ}\text{C}$ $T_C = 167^{\circ}\text{C}$	I_F	44 23 10	A
Non-Repetitive Forward Surge Current $T_C = 25^{\circ}\text{C}, t_p=8.3\text{ms}, \text{Half Sine Pulse}$ $T_C = 110^{\circ}\text{C}, t_p=8.3\text{ms}, \text{Half Sine Pulse}$	I_{FSM}	70 60	A
i^2t value $T_C = 25^{\circ}\text{C}, t_p=8.3\text{ms}, \text{Half Sine Pulse}$ $T_C = 110^{\circ}\text{C}, t_p=8.3\text{ms}, \text{Half Sine Pulse}$	$\int i^2 dt$	20 15	A^2s
Power dissipation $T_C = 25^{\circ}\text{C}$ $T_C = 110^{\circ}\text{C}$	P_{tot}	162 70	W
Operating junction Range	T_j	-55 to +175	$^{\circ}\text{C}$
Storage temperature Range	T_{stg}	-55 to +150	$^{\circ}\text{C}$

Thermal Resistance

Parameter	Symbol	Max.	Unit
Thermal resistance, junction – case.	R_{thJC}	0.92	°C/W

Electrical Characteristic (at $T_c = 25\text{ °C}$, unless otherwise specified)

Parameter	Symbol	Value			Unit	Test Condition
		min.	typ.	max.		
Forward Voltage	V_F	-	1.35	1.7	V	$I_F=10\text{A}$ $T_j=25\text{°C}$ $T_j=175\text{°C}$
Reverse Current	I_R	-	2	50	μA	$V_R=650\text{V}$ $T_j=25\text{°C}$ $T_j=175\text{°C}$
Total Capacitive Charge	Q_C	-	25	-	nC	$V_R=400\text{V}, T_j=25\text{°C}$ $Q_C = \int_0^{V_R} C(V) dV$
Total Capacitance	C	-	1220	-	pF	$T_j=25\text{°C}, f=1\text{MHz}$ $V_R=0\text{V}$ $V_R=200\text{V}$ $V_R=400\text{V}$

Characteristics Curve:

Fig 1: Forward Characteristics

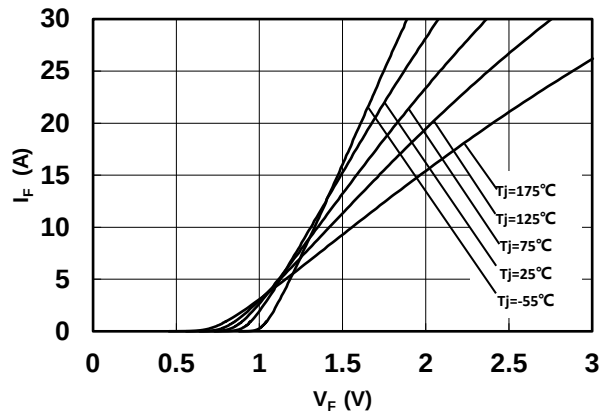


Fig 2: Reverse Characteristics

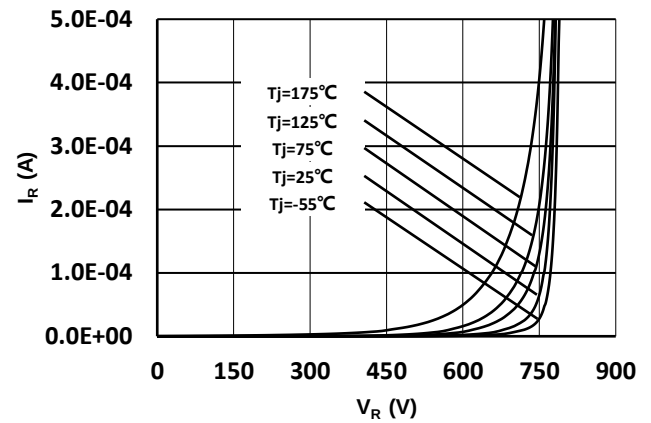


Fig 3: Current Derating

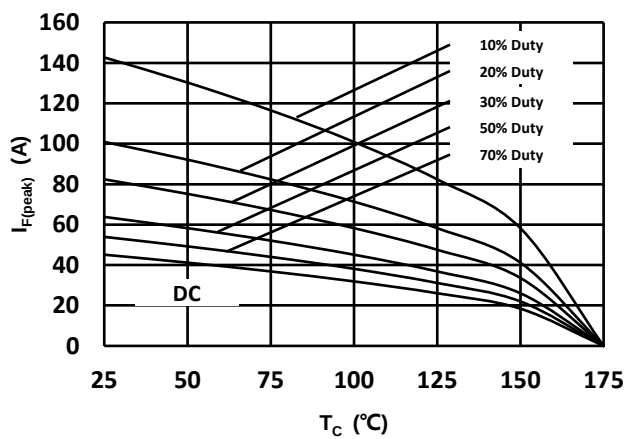


Fig 4: Power Derating

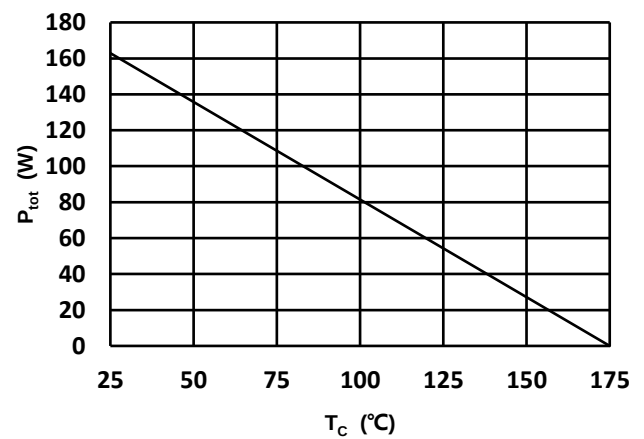


Fig 5: Capacitance vs. Reverse Voltage

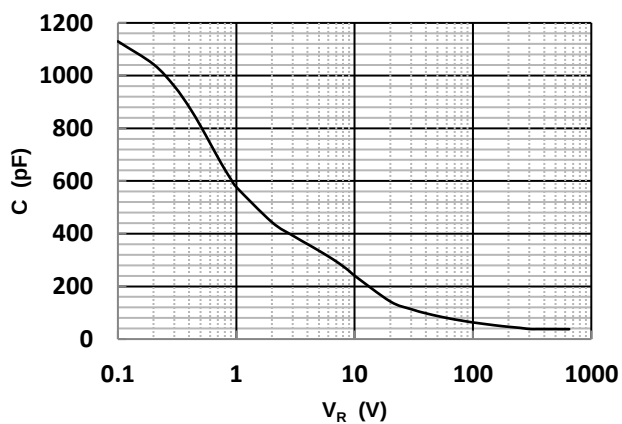


Fig 6: Reverse Charge vs. Reverse Voltage

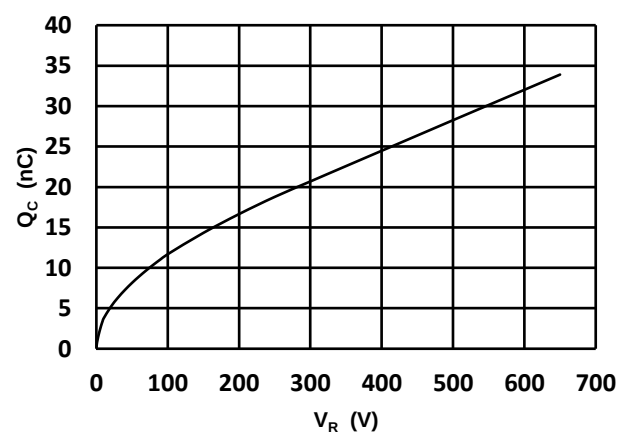


Fig 7: Typical Capacitance Stored Energy

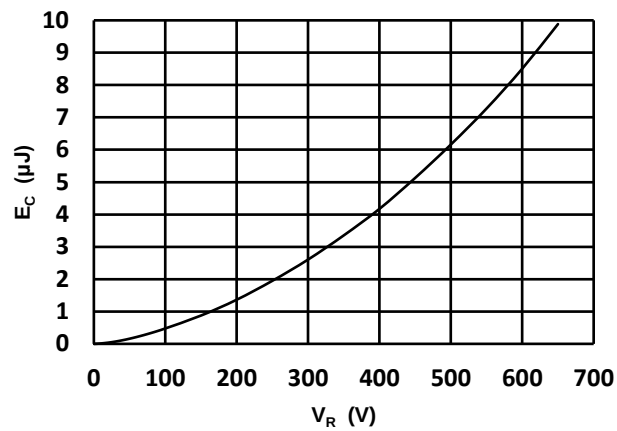
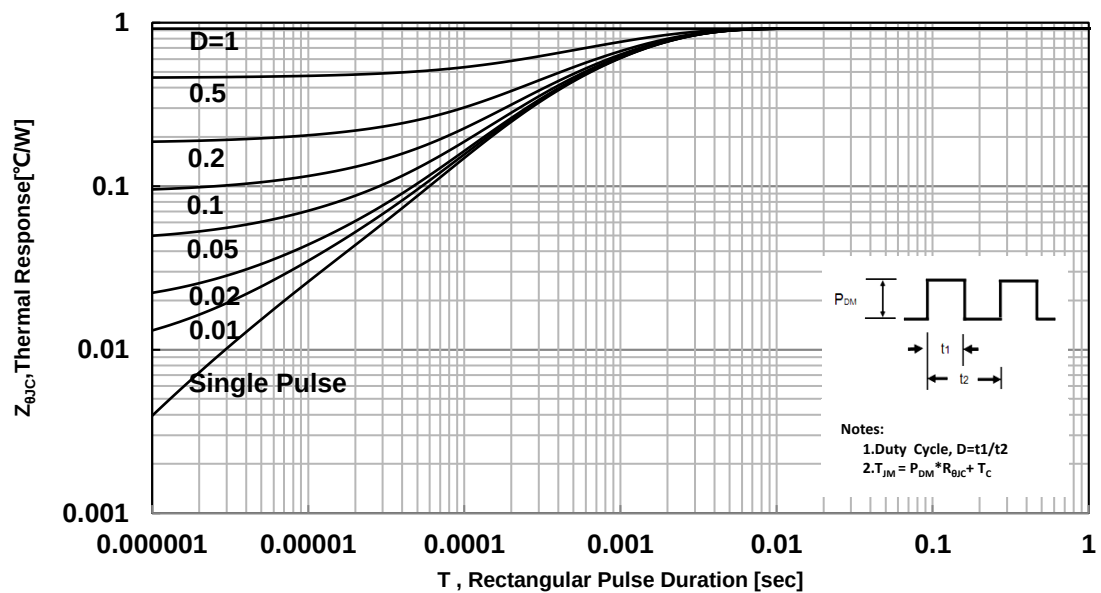
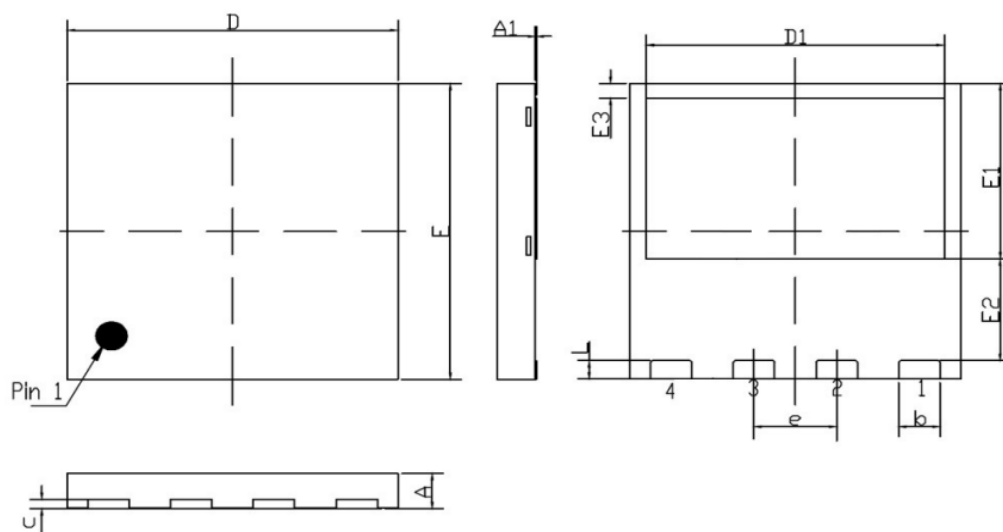


Fig 8: Transient Thermal Impedance



Package Outline: DFN8*8



Items	Values(mm)	
	MIN	MAX
A	0.75	1.10
A1	0.00	0.05
b	0.90	1.10
c	0.10	0.30
D	7.90	8.10
D1	7.10	7.30
E	7.90	8.10
E1	4.65	4.85
E2	2.65	2.85
E3	0.30	0.50
e	2.00BSC	
L	0.40	0.60

Revision History

Revision	Date	Major changes
1.0		Release of formal version.

Warnings

Exceeding the maximum ratings of the device in performance may cause damage to the device, even the permanent failure, which may affect the dependability of the machine. It is suggested to be used under 80 percent of the maximum ratings of the device.

1. When installing the heatsink, please pay attention to the torsional moment and the smoothness of the heatsink.
2. This product has not been designed or tested for use in, and is not intended for use in, applications implanted into the human body nor in applications in which failure of the product could lead to death, personal injury or property damage, including but not limited to equipment used in the operation of nuclear facilities, life-support machines, cardiac defibrillators or similar emergency medical equipment, aircraft navigation or communication or control systems, or air traffic control systems.