

Features

- CRM G4 SiC MOSFET Technology
- High Blocking Voltage with Low On-Resistance
- High Speed Switching with Low Capacitances
- Avalanche Ruggedness
- Fast Reverse Recovery
- Qualified according to JEDEC criteria

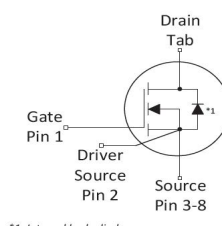
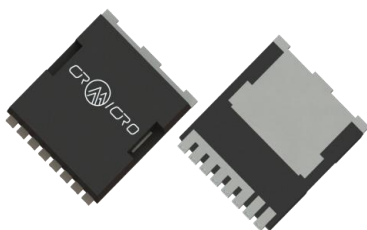
Applications

- Solar Inverters
- High Voltage DC/DC Converters
- On Board Charger(OBC)
- EV Charger

Product Summary

V _{DS}	1200V
R _{DS(on)_typ}	60m Ω
I _D	32A

100% Avalanche Tested



Package Marking and Ordering Information

Part #	Marking	Package	Packing	Qty
CRXZ60M120G4Z	XZ60M120G4Z	TOLL	Reel	2000pcs

Absolute Maximum Ratings

Parameter	Symbol	Value	Unit
Drain-source voltage	V _{DS}	1200	V
Continuous drain current V _{GS} =18V, T _C = 25°C V _{GS} =18V, T _C = 100°C	I _D	32 23	A
Pulsed drain current (T _C = 25°C, t _p limited by T _{jmax})	I _{D(pulse)}	80	A
Avalanche energy, single pulse (L=10mH, R _g =25 Ω)	E _{AS}	455	mJ
Gate-Source voltage (dynamic) ^{a1}	V _{GSmax}	-10/+22	V
Gate-Source voltage (static) ^{a2}	V _{GSop}	-5/+18	V
Power dissipation (T _C =25°C, T _J =175°C)	P _D	136	W
Operating Junction and Storage Temperature	T _j , T _{stg}	-55...175	°C

a1: When using MOSFET Body Diode V_{GSmax} = -5V/+22V

a2: MOSFET can also safely operate at 0/+18V

Thermal Resistance

Parameter	Symbol	Value	Unit
Thermal resistance, junction – case. Max	R_{thJC}	1.1	°C/W
Thermal resistance, junction – ambient. Max	R_{thJA}	50	

Electrical Characteristic (at $T_j = 25\text{ °C}$, unless otherwise specified)

Parameter	Symbol	Value			Unit	Test Condition
		min.	typ.	max.		

Static Characteristic

Drain-source breakdown voltage	$V_{(BR)DSS}$	1200	-	-	V	$V_{GS}=0V, I_D=250\mu A$
Gate threshold voltage	$V_{GS(th)}$	2	-	4	V	$V_{DS}=V_{GS}, I_D=5mA$
Zero gate voltage drain current	I_{DSS}	-	0.1	100	μA	$V_{DS}=1200V, V_{GS}=0V$ $T_j=25\text{ °C}$
		-	4	-		$T_j=175\text{ °C}$
Gate-source leakage current	I_{GSS}	-	-	± 100	nA	$V_{GS}=-10/+22V, V_{DS}=0V$
Drain-source on-state resistance	$R_{DS(on)}$	-	73	95	mΩ	$V_{GS}=15V, I_D=13.5A,$ $T_j=25\text{ °C}$
		-	110	-		$T_j=175\text{ °C}$
Drain-source on-state resistance	$R_{DS(on)}$	-	60	75	mΩ	$V_{GS}=18V, I_D=13.5A,$ $T_j=25\text{ °C}$
		-	108	-		$T_j=175\text{ °C}$
Transconductance	g_{fs}	-	12.6	-	S	$V_{DS}=20V, I_{DS}=13.5A$

Dynamic Characteristic

Internal Gate resistance	$R_{G(int)}$	-	3.4	-	Ω	f=1MHz
Input Capacitance	C_{iss}	-	1135	-	pF	$V_{GS}=0V, V_{DS}=1000V,$ f=1MHz
Output Capacitance	C_{oss}	-	48.5	-		
Reverse Transfer Capacitance	C_{rss}	-	2	-		
Coss Stored Energy	E_{oss}	-	29.6	-	μJ	
Gate Total Charge	Q_g	-	52.6	-	nC	VGS=-5/18V VDS=800V ID=13.5A
Gate-Source charge	Q_{gs}	-	18.7	-		
Gate-Drain charge	Q_{gd}	-	15	-		
Turn-on delay time	$t_{d(on)}$	-	5.8	-	ns	VDD=800V,ID=13.5A VGS=-5V/18V, RG=2.5 Ω , L=120uH
Rise time	t_r	-	9.3	-		
Turn-off delay time	$t_{d(off)}$	-	18.6	-		
Fall time	t_f	-	12.5	-		
Turn-On Switching Energy	$E_{(on)}$	-	35.8	-	μJ	
Turn Off Switching Energy	$E_{(off)}$	-	52.8	-		

Body Diode Characteristic

Parameter	Symbol	Value			Unit	Test Condition
		min.	typ.	max.		
Body Diode Forward Voltage	V_{SD}	-	3.6	-	V	$V_{GS}=0V, I_F=6.5A$
		-	3	-	V	$V_{GS}=0V, I_F=6.5A,$ $T_j=175^{\circ}C$
Continuous Diode Forward Current	I_S	-	-	32	A	$V_{GS}=0V$
Diode pulse Current	$I_{S,pulse}$	-	-	80	A	pulse width t_p limited by T_{jmax}
Body Diode Reverse Recovery Time	t_{rr}	-	15.4	-	ns	$di/dt=1000A/us$ $I_F=13.5A$ $V_{dd}=800V$ $T_j=25^{\circ}C$
Body Diode Reverse Recovery Charge	Q_{rr}	-	80	-	nC	
Body Diode Peak Reverse Recovery Current	I_{rrm}	-	8.3	-	A	
Body Diode Reverse Recovery Time	t_{rr}	-	17.6	-	ns	$di/dt=1000A/us$ $I_F=13.5A$ $V_{dd}=800V,$ $T_j=175^{\circ}C$
Body Diode Reverse Recovery Charge	Q_{rr}	-	90	-	nC	
Body Diode Peak Reverse Recovery Current	I_{rrm}	-	8.5	-	A	

Typical Performance Characteristics

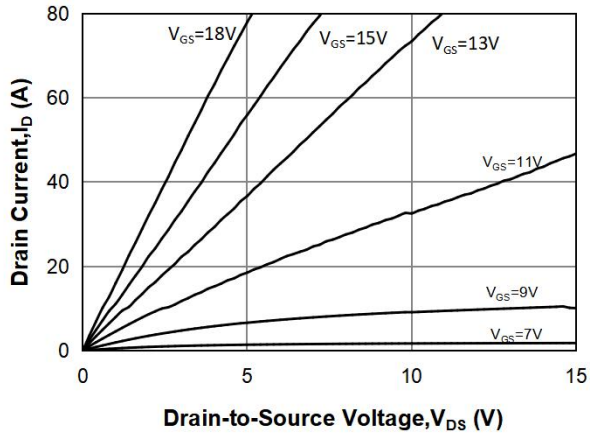
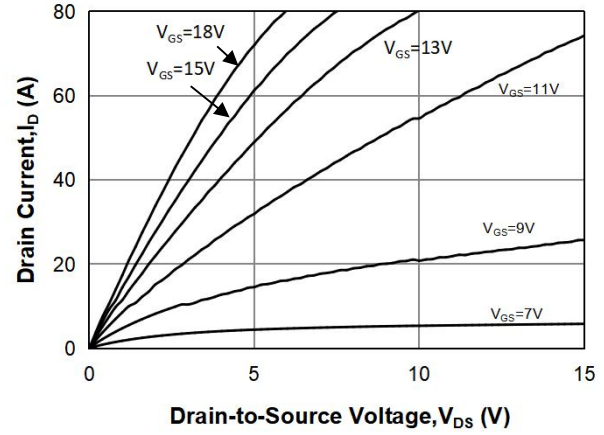
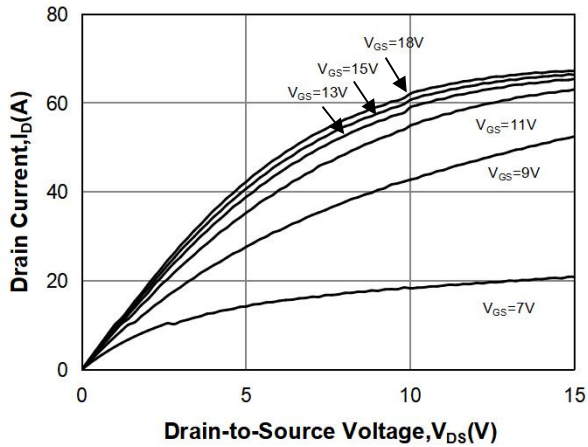
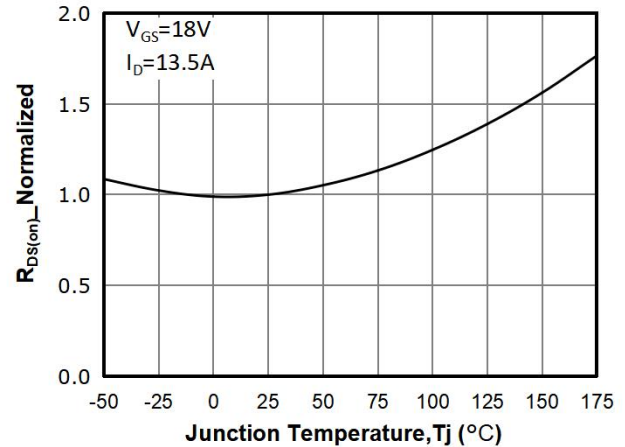
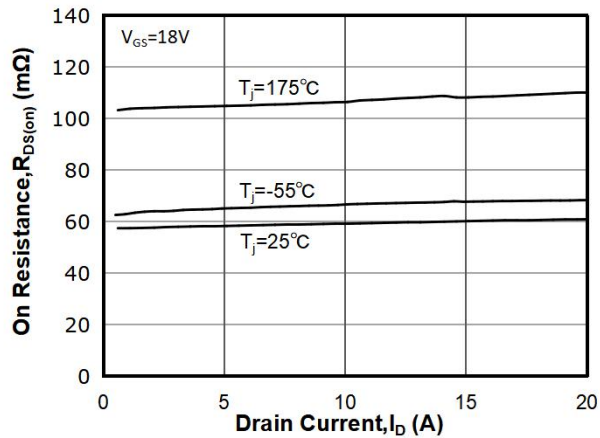
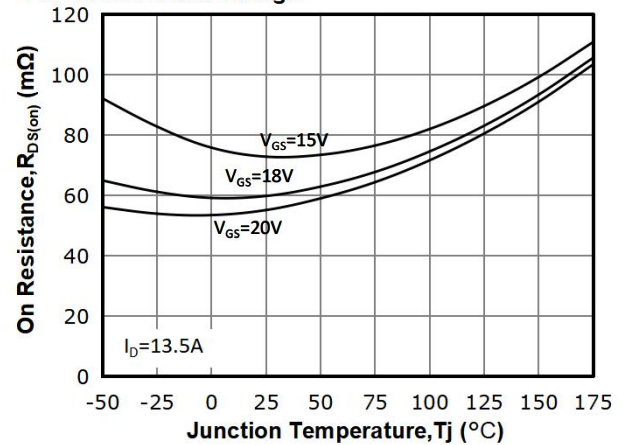
Fig 1. Output Characteristics ($T_j = -55^\circ\text{C}$)

Fig 2. Output Characteristics ($T_j = 25^\circ\text{C}$)

Fig 3. Output Characteristics ($T_j = 175^\circ\text{C}$)

Fig 4: $R_{ds(on)}$ vs. Temperature

Fig 5: On-Resistance vs. Drain Current For Various Temperatures

Fig 6: $R_{ds(on)}$ vs. Temperature For Various Gate Voltage


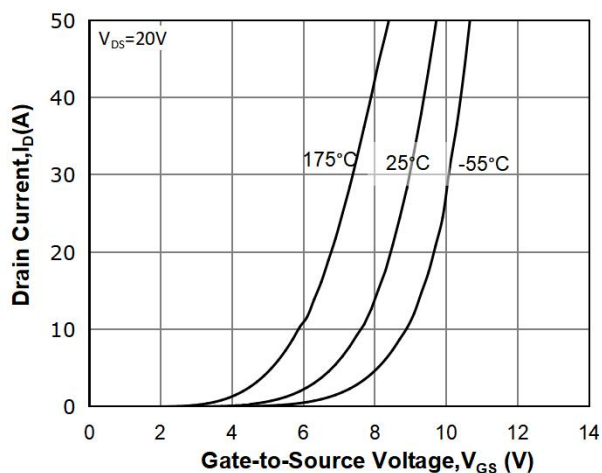
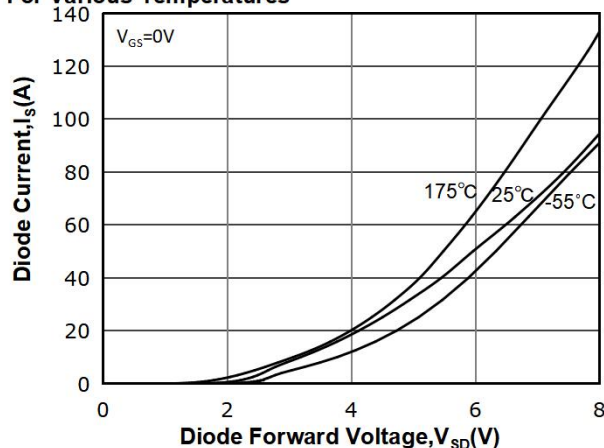
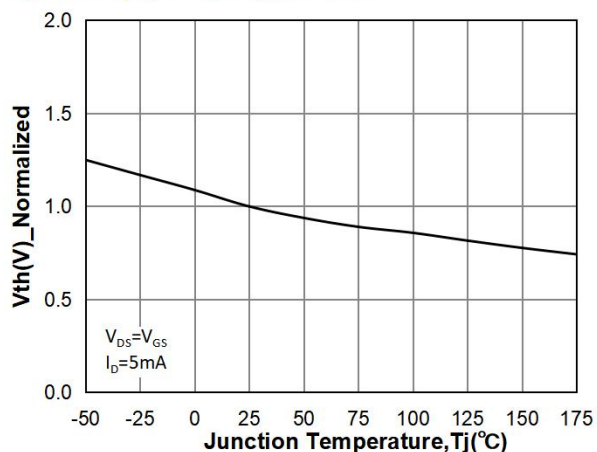
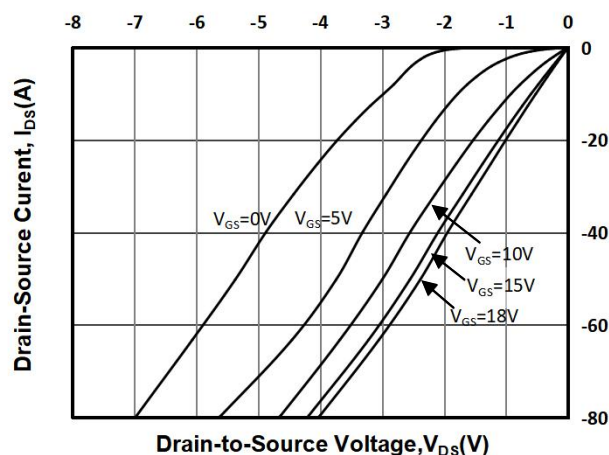
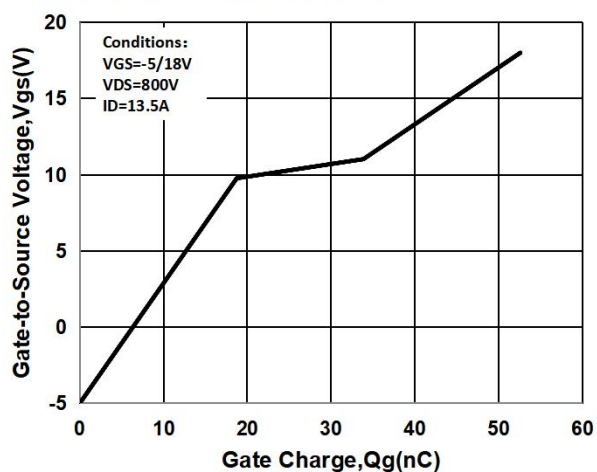
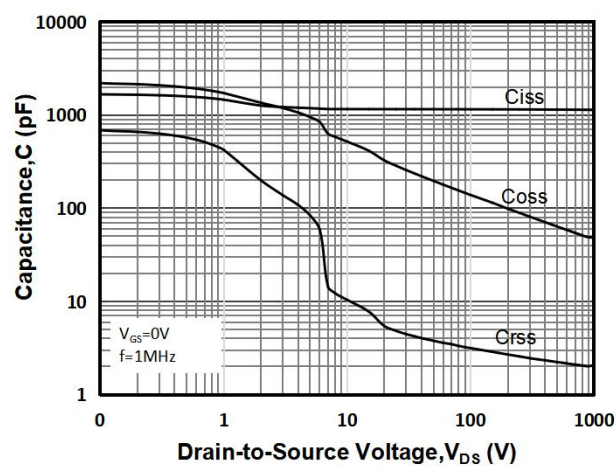
Fig 7: Transfer Characteristics

Fig 8: Body-diode Forward Characteristics For Various Temperatures

Fig 9: V_GS(th) Vs T_J Characteristics

Fig 10: 3rd Quadrant Characteristic at 25°C

Fig 11: Gate Charge Characteristics

Fig 12: Capacitance Characteristics


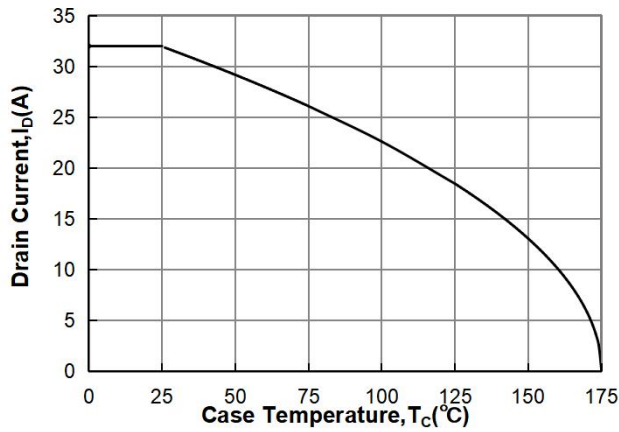
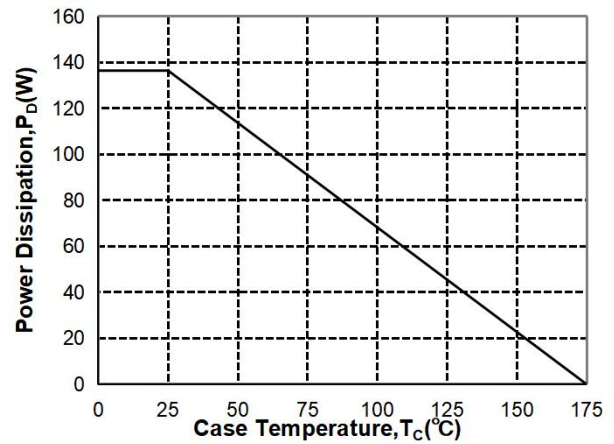
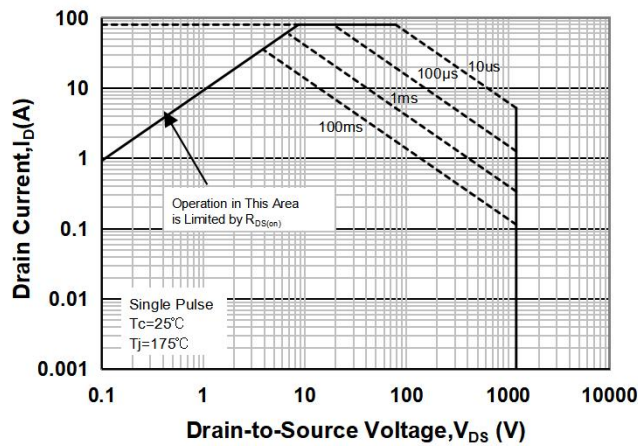
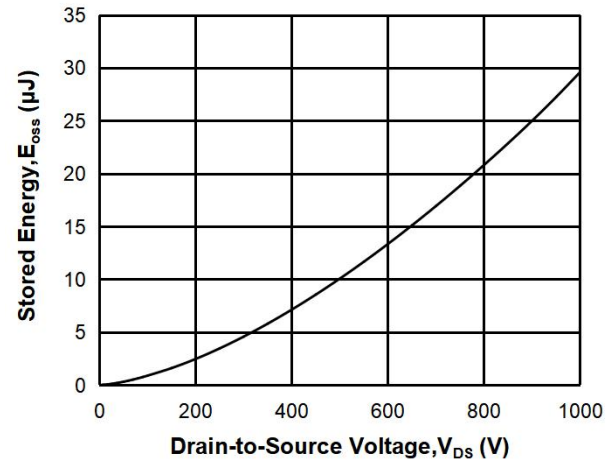
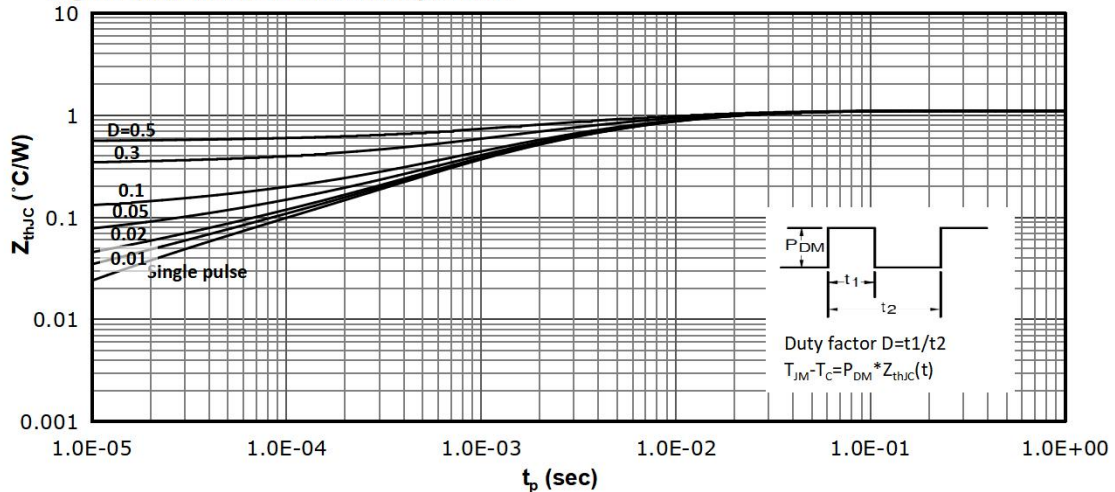
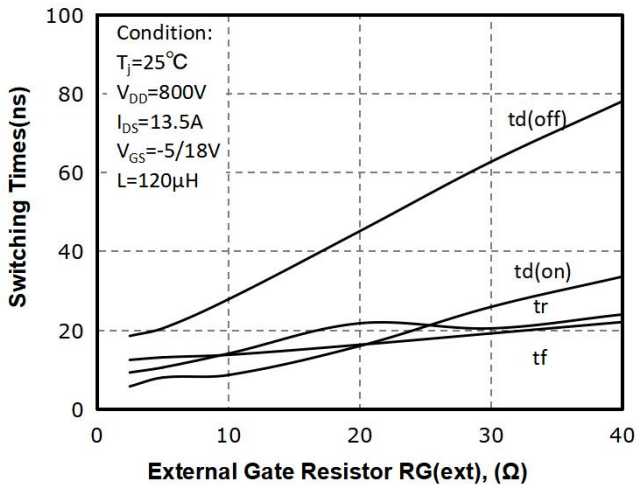
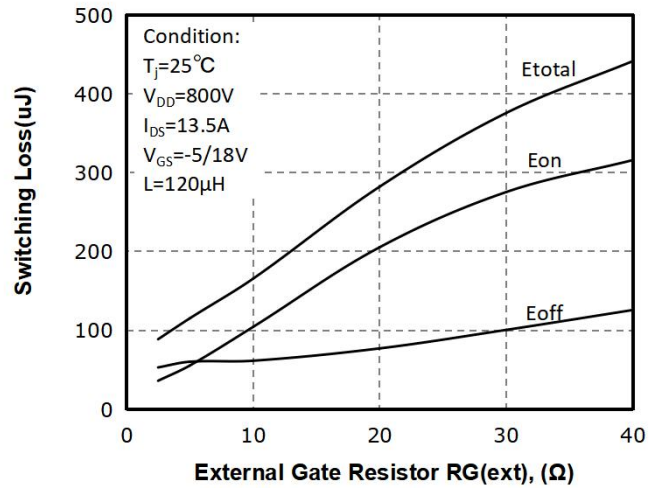
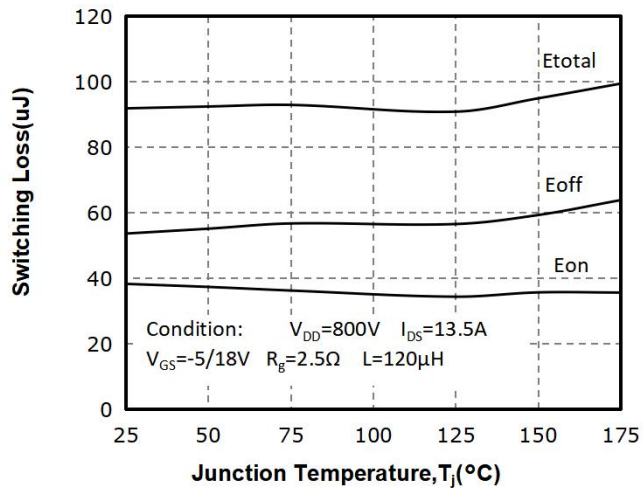
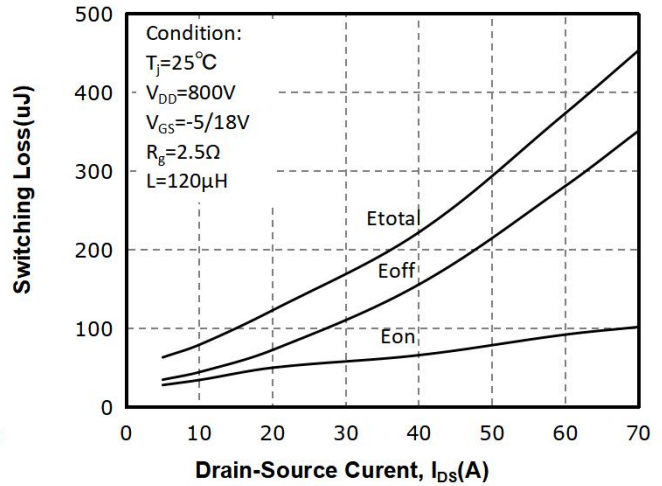
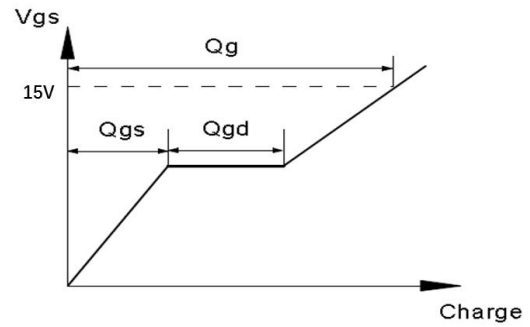
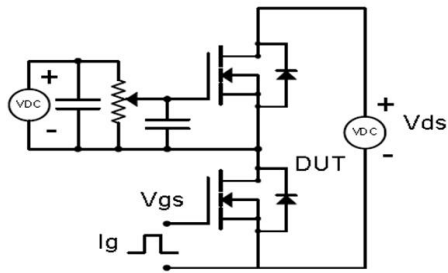
Fig 13: Continuous Drain Current vs. Case Temperature

Fig 14: Maximum Power Dissipation vs. Case Temperature

Fig 15: Safe Operating Area

Fig 16: Output Capacitor Stored Energy

Fig 17: Max. Transient Thermal Impedance


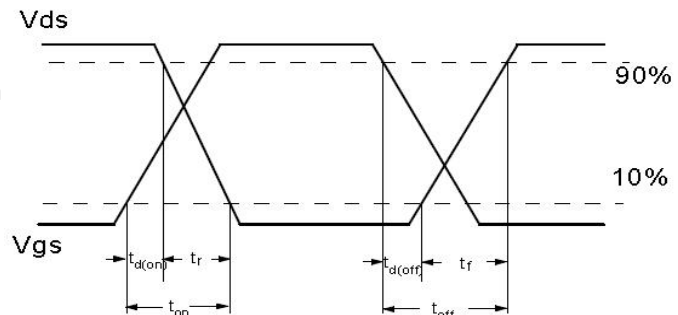
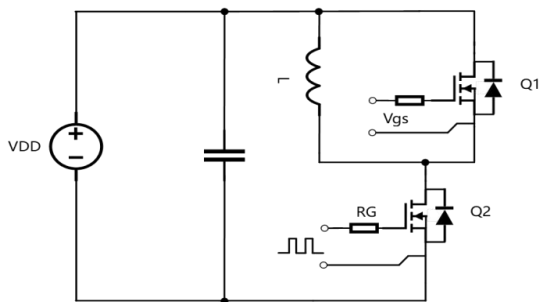
Fig 18: $R_g(\Omega)$ vs. Switching Times(us)

Fig 19: $R_g(\Omega)$ vs. Switching Loss(uJ)

Fig 20: Temperature($^\circ\text{C}$) vs. Switching Loss(uJ)

Fig 21: $I_{DS}(\text{A})$ vs. Switching Loss(uJ)


Test Circuit & Waveform

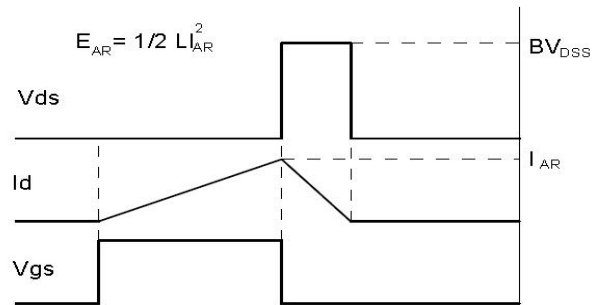
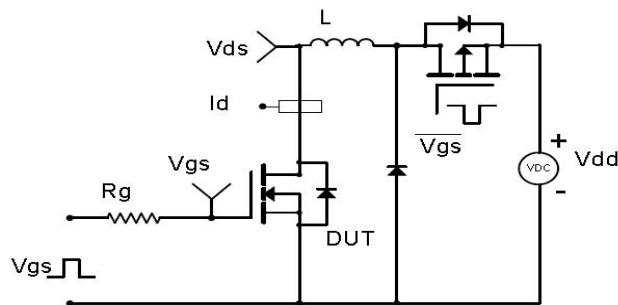
Gate Charge Test Circuit & Waveform



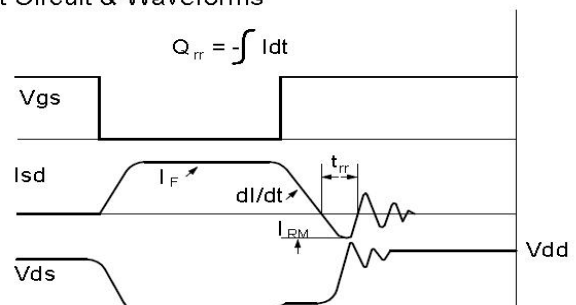
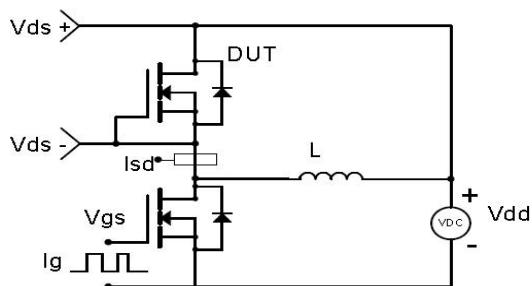
Resistive Switching Test Circuit & Waveforms

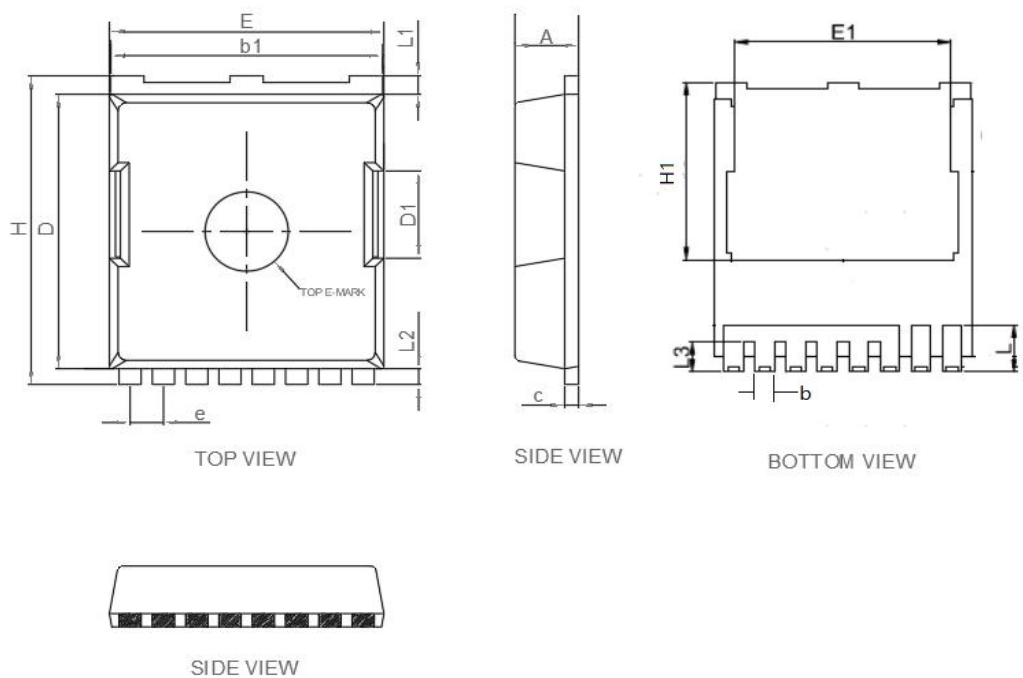


Unclamped Inductive Switching (UIS) Test Circuit & Waveforms



Diode Recovery Test Circuit & Waveforms



Package Outline: TOLL


Symbol	Values(mm)	
	MIN	MAX
A	2.15	2.45
b	0.7	0.9
b1	9.65	9.95
c	0.4	0.6
D	10.18	10.58
D1	3.15	3.45
E	9.7	10.1
E1	7.35	8.25
e	1.1	1.3
H	11.48	11.88
H1	6.8	7.5
L	1.6	2.1
L1	0.5	0.9
L2	0.45	0.75
L3	1	1.3

Revision History

Revision	Date	Major changes
1.0	2026/1/21	Release of formal version

Disclaimer

Unless otherwise specified in the datasheet, the product is designed and qualified as a standard commercial product and is not intended for use in applications that require extraordinary levels of quality and reliability, such as automotive, aviation/aerospace and life-support devices or systems.

Any and all semiconductor products have certain probability to fail or malfunction, which may result in personal injury, death or property damage. Customer are solely responsible for providing adequate safe measures when design their systems.

CRM reserves the right to improve product design, function and reliability without notice.